

What is claimed is:

1. An image sensor, comprising:

an image acquisition portion;

an image processing portion, receiving image information from said image acquisition portion, said image processing portion including a CMOS circuitry with CMOS outputs having an output impedance; and

an image receiving portion, having an input impedance, receiving said image information from said CMOS outputs, said image processing portion producing a current mode output and said image receiving portion receiving said current mode output; and

an impedance matching device, matching said output impedance of said image processing portion to said input impedance of said image receiving portion.

2. A sensor as in claim 1 wherein said image processing portion includes a portion with a CMOS output.

3. A sensor as in claim 1 wherein said impedance matching circuit comprises a circuit on said image processing circuit.

4. A sensor as in claim 3 wherein an output circuit of said image processing circuit includes a current biased transistor, wherein a magnitude of the current bias sets the output impedance.

5. A sensor as in claim 4 wherein said output impedance is matched to an input impedance of the image receiving circuit.

6. A sensor as in claim 1 wherein said impedance matching circuit comprises a circuit on said image receiving circuit.

7. A sensor as in claim 6 wherein an input circuit of said image receiving circuit includes a current biased transistor, wherein a magnitude of the current bias sets the input impedance.

8. A sensor as in claim 4 wherein said input impedance is matched to an output impedance of the image processing portion.

9. A sensor as in claim 1 wherein said impedance matching circuit comprises a first circuit on said image processing circuit and a second circuit on said image receiving circuit.

10. A sensor as in claim 9 wherein said first and second circuits include current biased elements, and wherein a magnitude of the current bias sets the output impedance.

11. A sensor as in claim 4 wherein said output impedance of said image processing circuit is matched to an input impedance of the image receiving circuit.

12. A sensor as in claim 1, wherein said image receiving circuit includes a current mirror part, that mirrors an input current.

13. A sensor as in claim 1 wherein said image acquisition circuit is an active pixel sensor with a photosensor, an in-pixel buffer, and an in pixel select transistor.

14. A sensor as in claim 13 wherein said an image acquisition portion and said image processing portion operates at substantially zero voltage.

15. An image sensor, comprising:
an image acquisition portion;
an image processing portion, receiving image information
from said image acquisition portion; and
an impedance matching device, matching said output impedance
of said image processing portion to said input impedance of said
image receiving portion by adjusting bias current through at
least one biased device in a way that renders the impedance
relatively independent of input current.

16. An image sensor as in claim 15, wherein said image
acquisition portion and said image processing portion each
operate in current mode.

17. An image sensor as in claim 16, wherein said portions
operate at substantially zero voltage.